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PATENT
Docket No.: ACT-299COA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2183

Examiner: Not Yet Assigned

Serial No.: 10/821,533

Filed: April 8, 2004

In re Application of: Kundu et al.

For: FIELD PROGRAMMABLE GATE ARRAY AND MICROCONTROLLER
SYSTEM-ON-A-CHIP

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 11/2/04, Signed Krista Thompson
Krista Thompson

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Each item of information listed in the attached FORM PTO-1449, for which a copy of each is attached, may be material to the examination of the above-identified application and is, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98. The Examiner is requested to make these items of official record in this application.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that any one or more of these items constitutes prior art.

I

This statement is filed pursuant to:

(X) 37 C.F.R. §1.97(b).

This information disclosure statement is filed either (1) within three months of the filing date of the national applications; (2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application; or (3) before the mailing date of a first office action on the merits, whichever event occurs last.

Accordingly, this information disclosure statement requires no fee and no certification.

() 37 C.F.R. §1.97(c).

This information disclosure statement is filed after the period specified in 37 C.F.R. §1.97(b), but before the mailing date of either (1) a final action under 37 C.F.R. §1.113 or (2) a notice of allowance under 37 C.F.R. §1.311.

Accordingly, this information disclosure statement requires either the fee specified in 37 C.F.R. §1.17(p) or a certification according to 37 C.F.R. §1.97(e).

() 37 C.F.R. §1.97(d).

This information disclosure statement is filed after the period specified in 37 C.F.R. §1.97(c).

Accordingly, this information disclosure statement requires the fee specified in 37 C.F.R. §1.17(p), \$180.00, for submission of an information disclosure statement under 37 C.F.R. §1.97(d), and a statement according to 37 C.F.R. §1.97(e).

37 C.F.R. §1.97(e).

- () (1) Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.

- () (2) No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. §1.56(c), more than three months prior to the filing of the statement.

Accordingly, this information disclosure statement requires the fee specified in 37 C.F.R. §1.17(p), \$180.00, for submission of an information disclosure statement under 37 C.F.R. §1.97(e).

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. §1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record. To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.

II

(X) No fee is due.

() The fee specified in 37 C.F.R. §1.17(p) for submission of an information disclosure statement under 37 C.F.R. §1.97(c), 37 C.F.R. § 1.97(d), or 37 C.F.R. §1.97(e) is enclosed, \$180.00.

In the event any fee is required for filing the above-noted document, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing attached document timely, the Assistant Commissioner is hereby authorized to charge the fee to our Deposit Account No. 50-0612. A duplicate of this page is enclosed.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.

A handwritten signature in black ink, appearing to read 'Kenneth D' Alessandro', with a long horizontal flourish extending to the right.

Kenneth D' Alessandro
Reg. No.: 29,144

Dated: November 2, 2004

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Krista Thompson

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find the following:

1. Information Disclosure Statement;
2. Form PTO-1449
3. Copies of 27 References.

In the event any additional fee is required for filing the above-noted document, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of attached document timely, the Assistant Commissioner is hereby authorized to charge the fee to our Deposit Account No.: 50-0612. A duplicate copy of this page is enclosed.

Respectfully submitted,
Sierra Patent Group, Ltd.

Kenneth D. Alessandro
Reg. No.: 29,144

Dated: November 2, 2004

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Form PTO 1449 (Rev. 2-32)		U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. ACT-299COA	Serial No. 10/821,533
Information Disclosure Statement by Applicant			Applicant: Kundu et al.	
(Use several sheets if necessary)			Filed: April 8, 2004 Group: 2183	
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)				
	18	Triscend E5 Configurable System-on-Chip Family, Triscend Corporation, 1-90, i-ii, 2000 (no month).		
	19	AN07 Using Keil Development tools with Triscend FastChip and the E5 CsoC Family, Triscend Corporation, pp. 1-20, 2000 (no month).		
	20	CY7C64013, CY7C64113 Full-speed USB (12 Mbps) Function Preliminary, Cypress Semiconductor Corporation, pp. 1-2, February 4, 2000.		
	21	Phil Harvey, "Triscend leads a hybrid chip charge", [Internet] Inside Tech column, <u>Red Herring</u> , pp. 1-3, February 4, 2000.		
	22	Inventra™ M1284H-A1 Host Parallel Port, Datasheet, [Internet] www.mentor.com/inventra , Mentor Graphics, pp. 1 & 2, June 2000.		
	23	Inventra™ M8051 E-Warp 8-Bit Microcontroller with On-Chip Debug, [Internet], www.mentor.com/inventra , Mentor Graphics, pp. 1 & 2, June 2000.		
	24	Markus Levy, "Processors drive (or dive) into programmable-logic devices", <u>EDN</u> , pp. 107-108, 110, 112, 114, July 20, 2000.		
	25	Field Marshal P200 Technology Overview, [Internet] http://www.tellima.co.uk/proj200view.htm , pp. 1-2, July 26, 2000.		
	26	All about UARTs, [Internet] http://consumer.3com.com/modem/documents/10589.htm , 3Com Corp., pp. 1-2, July 26, 2000.		
	27	PCT/US 01/27130, ACT-299PCT, Actel Corporation, copy of International Search Report, 5 pages, mailed 8/25/2003.		
Examiner			Date Considered	
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.				